What is claimed is:

1. A driving method of a plasma display panel for driving gradation-wise a plasma display panel having a plurality of discharge cells each arranged in matrix and bearing a role of a pixel by constituting one field of input image signals by a plurality of sub-fields, comprising:

setting each of said discharge cells to one of a light emission cell state and a light non-emission cell state in accordance with said input image signal in each of said sub-fields; and

causing only said discharge cell under said light emission cell state to emit light a number of light emissions allotted in accordance with weighting of said sub-field, wherein the number of light emissions to be allotted in accordance with weighting of said sub-field is rendered different for each of said discharge cells inside a discharge cell block consisting of a plurality of said discharge cells adjacent to one another.

2. A driving method of a plasma display panel according to claim 1, wherein said number of light emissions to be allotted to each of said discharge cells inside said discharge cell block is varied for each field.

3. A driving method of a plasma display panel for driving gradation-wise a plasma display panel having

a plurality of discharge cells each arranged in matrix and bearing a role of a pixel by constituting one field of input image signals by a plurality of sub-fields, comprising the following steps serially conducted in each of said sub-fields:

a pixel data write step for setting each of said discharge cells to one of a light emission cell state and a light non-emission cell state in accordance with said input image signal;

a first light emission sustain step for causing only said discharge cell under said light emission cell state among said discharge cells to emit light the number of light emissions corresponding to weighting of said sub-field;

a first selective erase step for compulsively bringing only said discharge cell positioned at a first position inside said discharge cell block consisting of four of said discharge cells adjacent to one another into said light non-emission cell state;

a second light emission sustain step for causing said discharge cells under said light emission cell state among said discharge cells to emit light a predetermined number of times;

a second selective erase step for compulsively bringing only said discharge cell positioned at a second position inside said discharge cell block

into said light non-emission cell state;

a third light emission sustain step for causing only said discharge cells under said light emission state among said discharge cells to emit light a predetermined number of times;

a third selective erase step for compulsively bringing only said discharge cell arranged at a third position inside said discharge cell block into said light non-emission cell state; and

a fourth light emission sustain step for causing only said discharge cells under said light emission cell state among said discharge cells to emit light a predetermined number of times.